

## PMG1-S1 DRP REFERENCE DESIGN

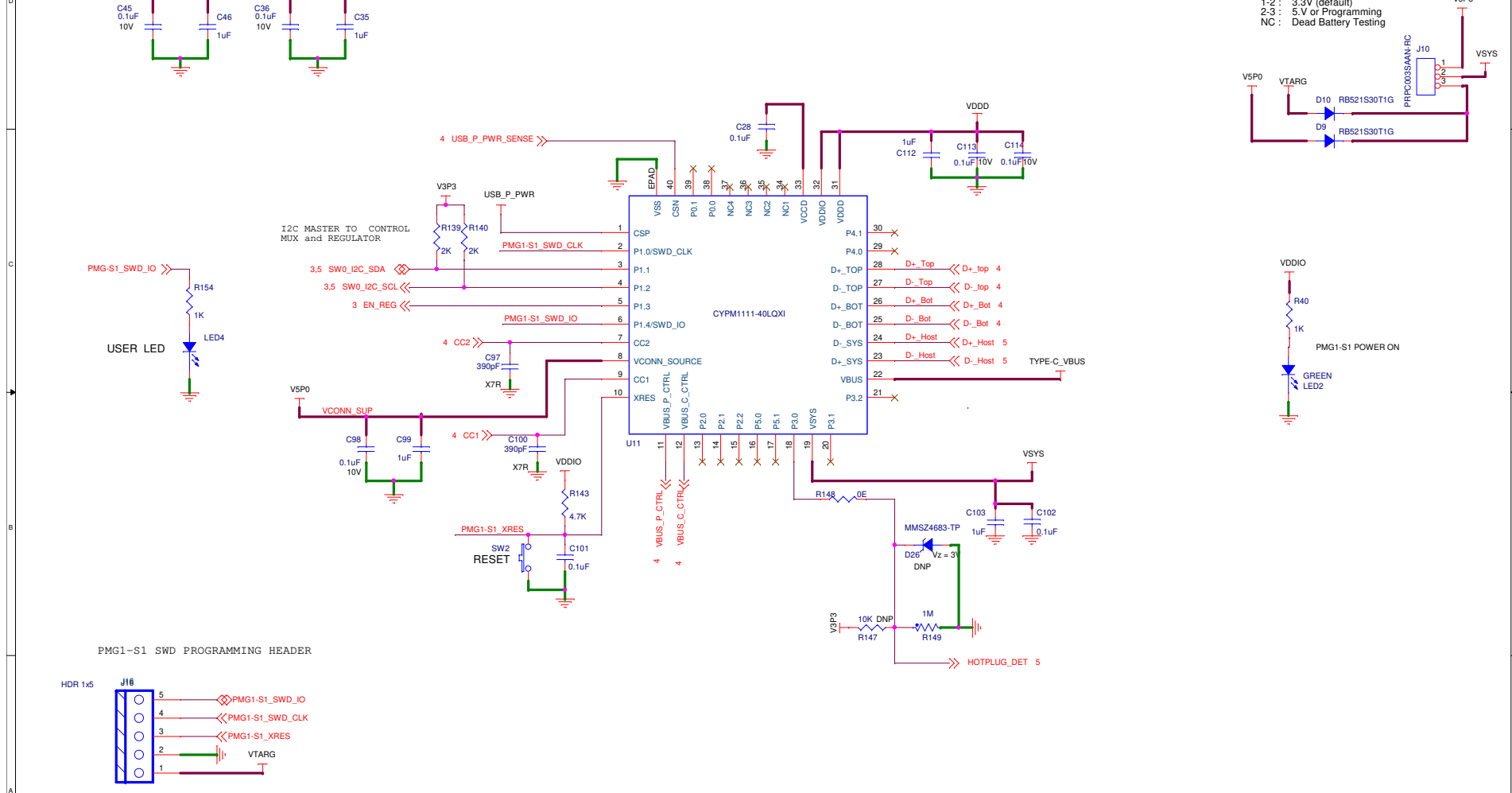
CONTENTS	
PAGE	DESCRIPTION
01	TITLE & TABLE OF CONTENTS
02	PMG1-S1 CONTROLLER
03	STEP DOWN REGULATORS
04	PD REGULATOR
05	VBUS PROVIDER/CONSUMER FET AND TYPE-C CONNCETOR
06	USB SUPERSPEED AND DISPLAY PORT MUX (Optional)

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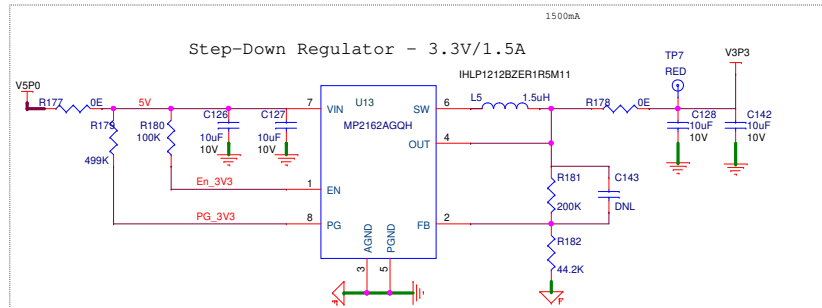
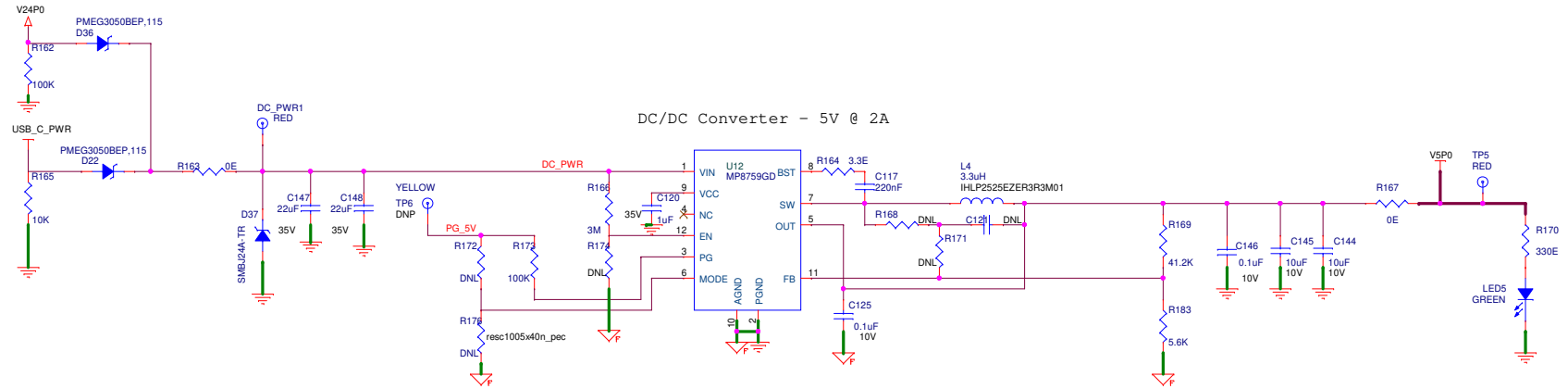
SCH Title : PMG1-S1 DRP REFERENCE DESIGN				
Page Title: TITLE AND TABLE OF CONTENT				
Size A3	Document Number 630-XXXX-XX	Drawn By SRISHTI	Approved By VIKU	Rev 01
Date: Wednesday, April 27, 2022		Sheet 1 of 6		

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Date: Wednesday, April 27, 2022 Sheet 2 of 6

# STEP DOWN REGULATORS



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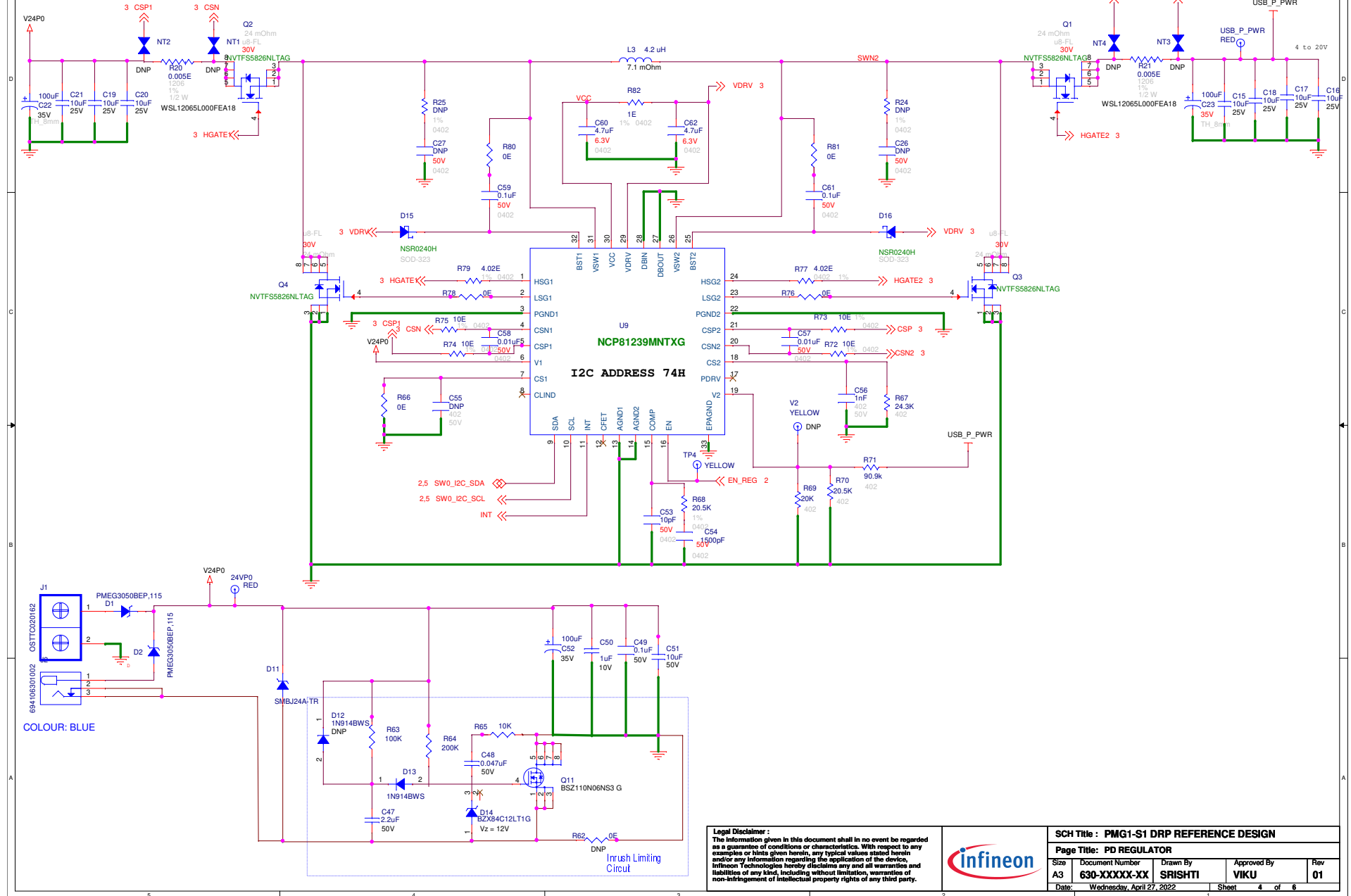


SCH Title : PMG1-S1 DRP REFERENCE DESIGN

Page Title: STEP DOWN REGULATORS

Size	Document Number	Drawn By	Approved By	Rev
A3	630-XXXX-XX	SRISHTI	VIKU	01
Date:	Wednesday, April 27, 2022	Sheet	3	of 6

## PD REGULATOR



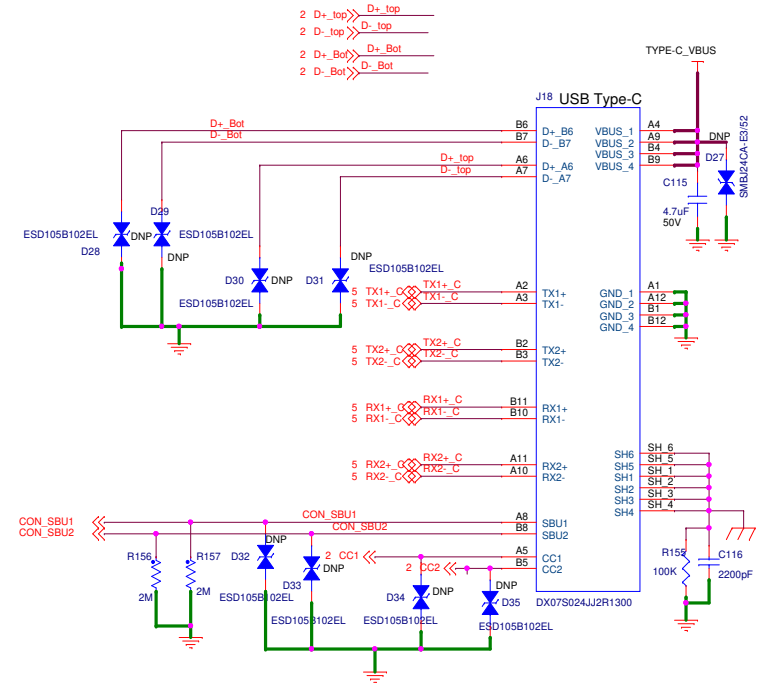
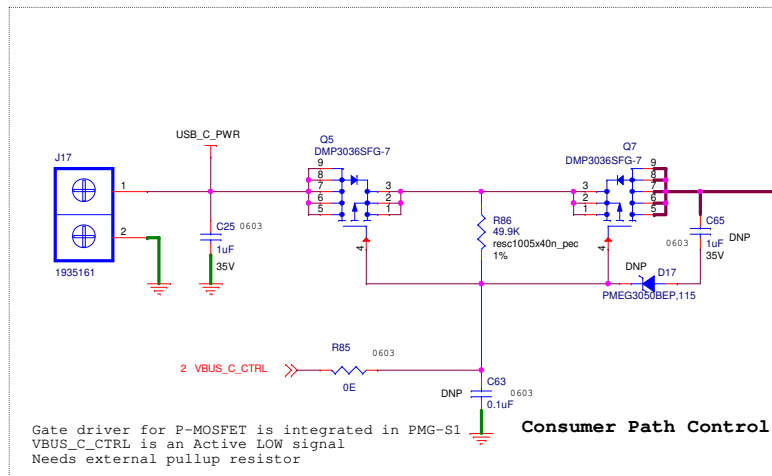
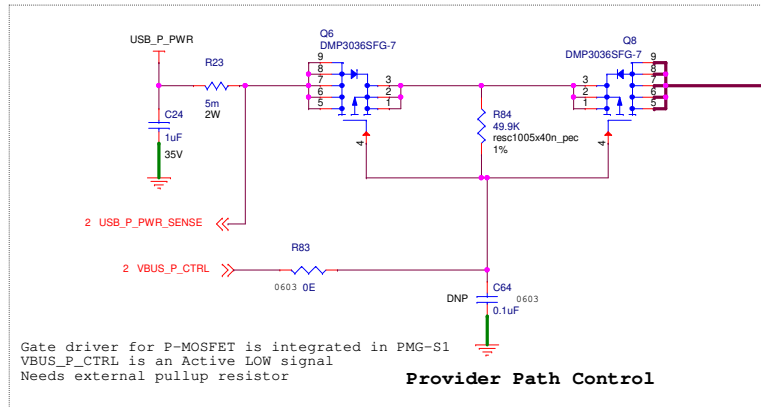
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SCH Title : PMG1-S1 DRP REFERENCE DESIGN

Page Title: PD REGULATOR

Size A3	Document Number <b>630-XXXX-XX</b>	Drawn By <b>SRIшти</b>	Approved By <b>VIKU</b>	Rev <b>01</b>
Date: Wednesday, April 27, 2022		Sheet 4 of 6		

# VBUS PROVIDER/CONSUMER FET AND TYPE-C CONNECTOR



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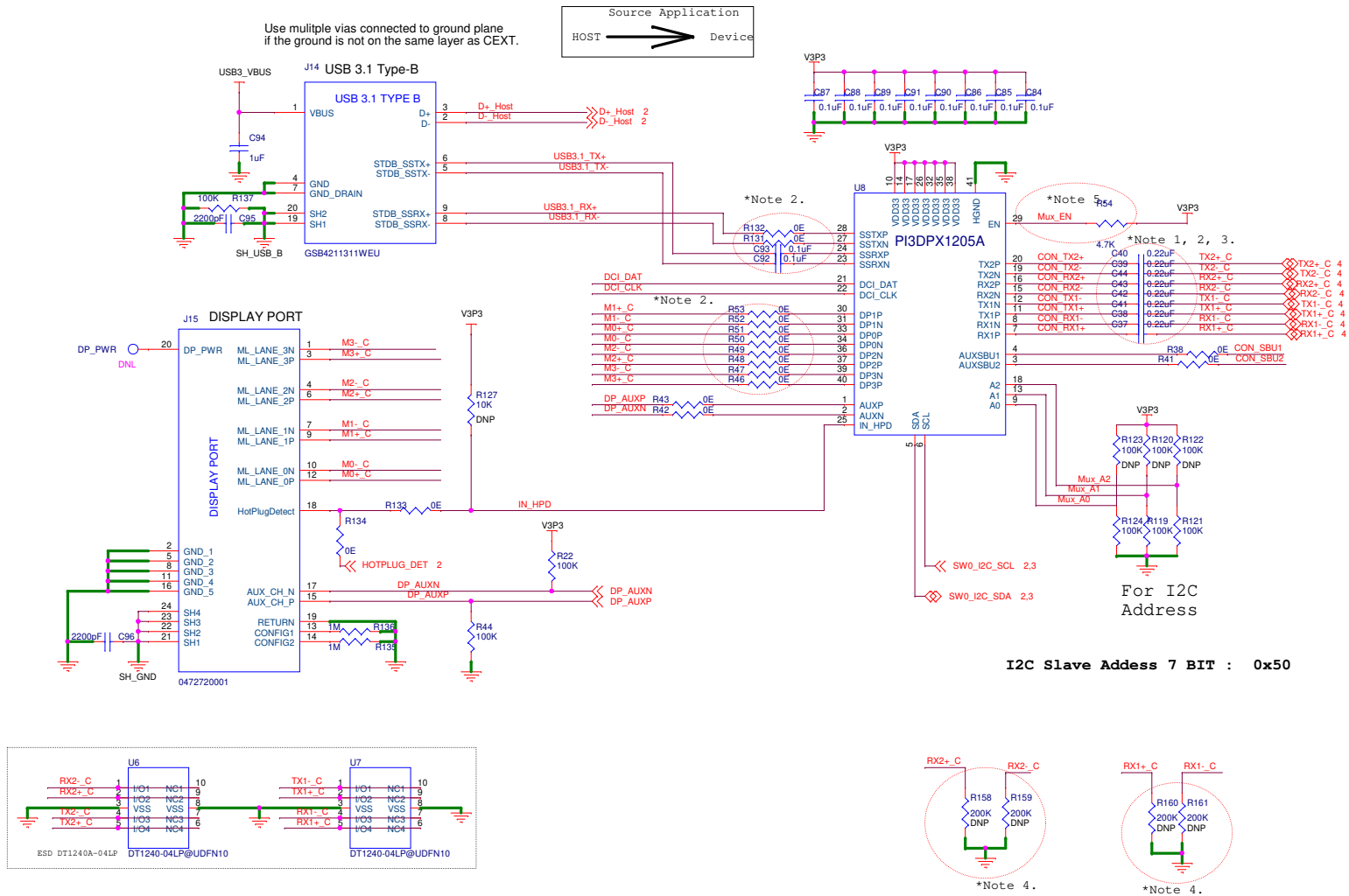
SCH Title : PMG1-S1 DRP REFERENCE DESIGN

Page Title : VBUS PROVIDER/CONSUMER FET AND TYPE-C CONNECTOR

Size	Document Number	Drawn By	Approved By	Rev
A3	630-XXXX-XX	SRISHTI	VIKU	01

Date: Wednesday, April 27, 2022 Sheet 5 of 6

## USB SUPERSPEED AND DISPLAY PORT MUX (OPTIONAL)



- \*Note 1: This section is required for Design that requires USB3 and DP Alt mode Support .
- \*Note 2: Suggest RX1/2 signals are reserved AC coupling to support non-compliant Type-C device.  
RX1/2 AC cap C120, C121, C124, C125 should be replaced by 0ohm resistors when DCI test.
- \*Note 3: Recommended AC coupling cap characters: 0201 or 0402, X5R/X7R.
- \*Note 4: Suggest to 0.22uF AC coupling cap to ensure risk free in USB-IF Compliance.
- \*Note 5: Reserve 200kohm to support non-compliant Type-C device.
- \*Note 6: Suggest MUX\_EN to be controlled by GPIO.

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<b>SCH Title : PMG1-S1 DRP REFERENCE DESIGN</b>				
<b>Page Title: USB SUPERSPEED AND DISPLAY PORT MUX (OPTIONAL)</b>				
Size	Document Number	Drawn By	Approved By	Rev
A3	630-XXXX-XX	SRISHTI	VIKU	01
Date: Wednesday, April 27, 2022		Sheet 6 of 6		